



PRIYADARSHINI ENGINEERING COLLEGE

(Approved by AICTE, New Delhi, Affiliated to Anna university-Chennai)

DEPARTMENT OF ECE Organizing

Workshop On “PCB DESIGN”

10th March 2014

PROGRAMME REPORT

Department of ECE on 10th March 2014 organized workshop on “PCB Design”. Resource person **Mr.Sabarinathan**, Managing Director, Vellore. Head of the Department **Mrs.N.Jayakala** Id. Head, Electronics and Communication Engineering Department, gave the introductory speech about the schedule and key features of the Program. Coordinator of this program proposed vote of thanks for this Inaugural session.

The main objective of the workshop is to train the Technicians and students to understand the concepts related to PCB Making using NI Multisim and NI Ultiboard.

The workshop is envisioned to provide a unique bilateral forum to establish conversation and idea exchange amongst the resource persons and fellow faculty which helps to improve their skill set related to circuit designing.

This program would have a serial activity of events for three days which revolve around finding and sharing solutions for typical challenges in the field circuit designing.

Topics Covered in the workshop included the following:

- Introduction to. Graphical system design
- Hands on Lab view programming concepts
- Introduction to Multisim and Circuit design
- NI Labview and Multisim co-simulation
- Hands on Circuit Designing on Multisim
- Introduction to NI Ultiboard
- PCB Design for Multisim circuits